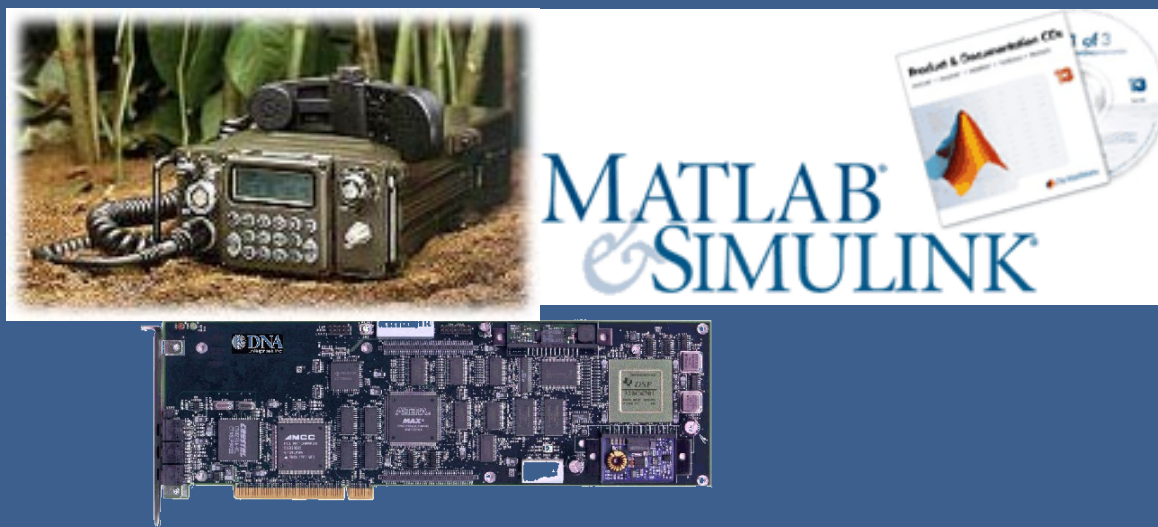


JTRS JPO SCA Extension Workshop

SDR Code Portability with MATLAB and Simulink

Steven Conahan

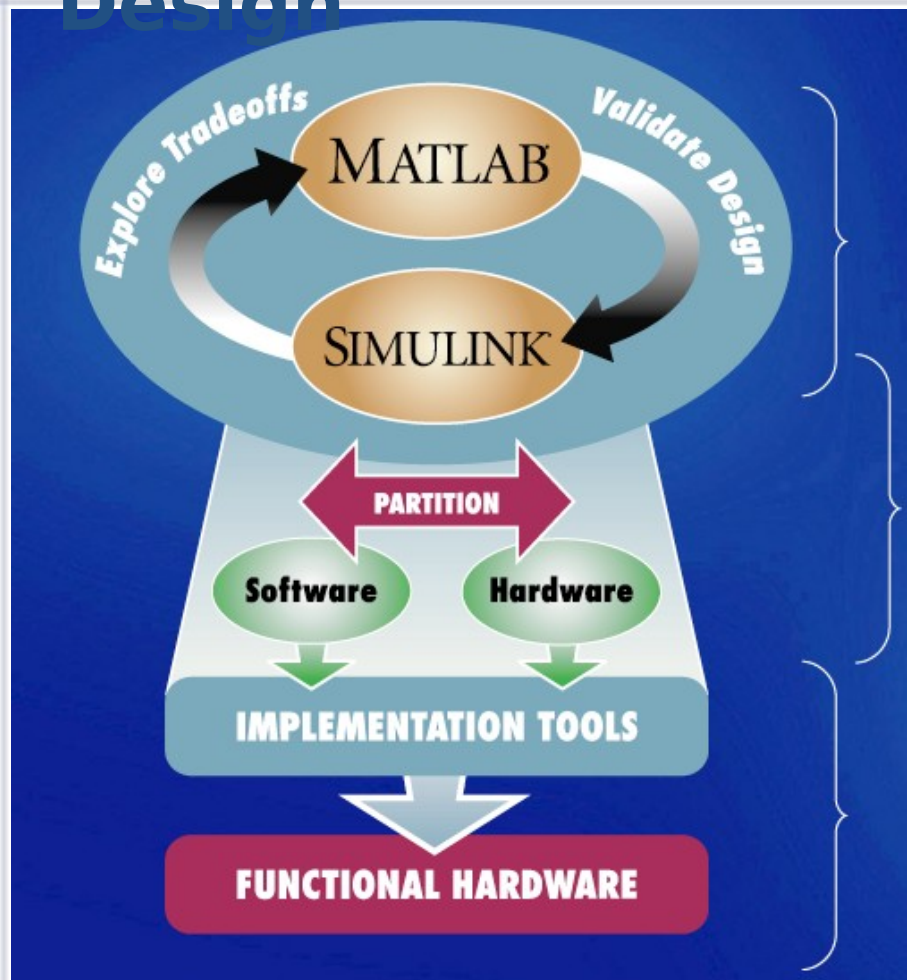
Brian Ogilvie



Signal Processing and
www.mathworks.com

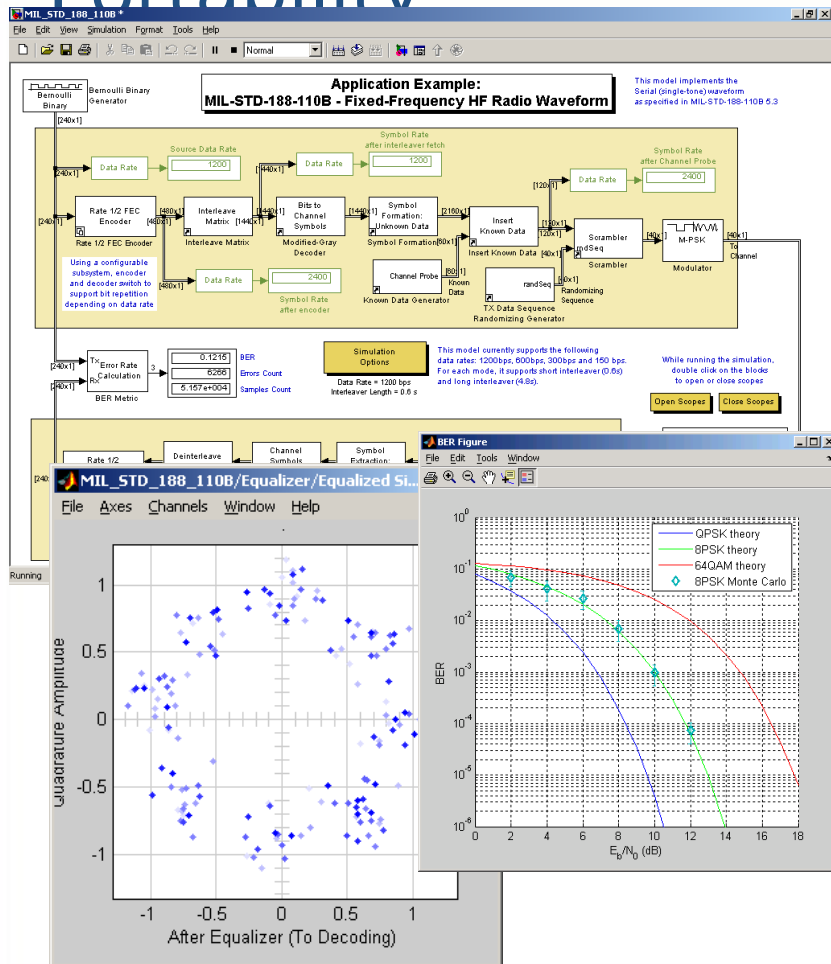
Communications

MathWorks Platform for Model-Based Design



- Use Simulink model as reference and executable specification
- Create and validate design
- Detect design flaws early
- Reduce risk and time-to-market
- Partition the design
- Add target-specific implementation detail (e.g. TI, Altera, Xilinx)
- Complete design flow to embedded hardware (FPGA + DSP + Microprocessor)

MathWorks Capabilities for SDR Code Portability



- Framework for sharing specs and system requirements
- Extensible IP block libraries, software APIs, data type support
- Open, customizable, extensible
- De facto industry standard for numerical computing

Code Generation + Verification and Validation

- Hardware-independent design, simulation, code generation
- Embedded code generation for Microprocessors, DSPs, FPGAs
- Open verification tools platform, test bench portability
- Large third-party support



Weaver's "Third Method" of SSB Generation TX
A Third Method of Generation and Detection
of Single-Sideband Signals
Donald K. Weaver Jr., Proc. IRE, Dec. 1958, pp. 1703-1705

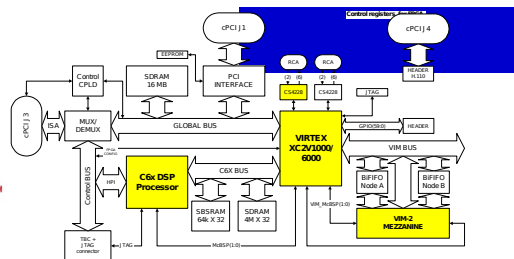
Bandlimited audio input (250-3250 Hz) is translated
Simulink HF SSB Transceiver

Red = 64MHz
SSB Rx/Tx Part I: FPGA Frequency Translator
0.5-29 MHz, Fs=64MSPS

RF in

SSB Rx/Tx Part II: TI DSP Modulator / Demodulator

Interface to - Host FPGA
Digital Frequency Translator



Code Composer
Studio



Model
Sim®



XILINX
SYSTEM
GENERATOR®
For Simulink®

Examples of Signal Processing Applications

- Both Xilinx and Altera have chosen to build their signal processing strategy on the Simulink platform.
- “Sandia Implements a High-Performance Radar Receiver Using MathWorks and Xilinx DSP Design Tools.”
- “Lockheed Martin Aeronautics and The MathWorks Achieve Milestone With Successful F-35 JSF Flight Simulator Test. The MathWorks Software and Model-Based Design Approach Used to Design, Code, and Test Flight-Control System.”



Recommendations to JTRS JPO

- Use **Simulink** to completely specify the Signal Processing Subsystem (SPSS) **without** locking into a specific vendor or technology.
- Vendors and users can then optimize per platform for specific FPGA, DSP, or Microproc technology targets.
- SCA: Any contractor can implement any part of the SPSS from the executable **Simulink** specification.

Everyone has equal footing.

Simulink is the ideal design portability platform.